

CLAIMS

What is claimed is:

1. A pixel interpolation circuit for generating interpolation pixel data which interpolates an input image based on pixel data
5 composing the input image, the pixel interpolation circuit comprising:

a plurality of interpolation circuits each calculating interpolation candidate data of a interpolation pixel and test interpolation data of a plurality of pixels neighboring the
10 interpolation pixel, using different interpolation methods;

a determining circuit for selecting one of the interpolation circuits based on a difference between the test interpolation data and actual pixel data; and

an output circuit for outputting the interpolation candidate
15 data calculated by the selected interpolation circuit as the interpolation pixel data.

2. A pixel interpolation circuit according to claim 1, wherein the determining circuit calculates a evaluation data for each of the interpolation circuits, by summing up the absolute values of
20 the difference between the test interpolation data and the actual pixel data, and selects one of the interpolation circuits based on the evaluation data.

3. A pixel interpolation circuit according to claim 1, wherein the determining circuit calculates binarized or ternarized values
25 of the difference between the test interpolation data and the actual pixel data.

4. A pixel interpolation method for generating interpolation pixel data which interpolates an input image based on pixel data composing the input image, the pixel interpolation method comprising:

calculating interpolation candidate data of a interpolation pixel and test interpolation data of a plurality of pixels neighboring the interpolation pixel, using different interpolation methods;

selecting one of the interpolation methods based on a difference between the test interpolation data and actual pixel data; and

outputting the interpolation candidate data calculated by the selected interpolation method as the interpolation pixel data.

5. A pixel interpolation method according to claim 4 further comprising,

calculating a evaluation data for each of the interpolation methods, by summing up the absolute values of the difference between the test interpolation data and the actual pixel data, wherein the interpolation method is selected based on the evaluation data.

6. A pixel interpolation circuit according to claim 4 further comprising,

calculating binarized or ternarized values of the difference between the test interpolation data and the actual pixel data.

7. An image scanner comprising a pixel interpolation circuit according to claims 1.